

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Yoshiichiro KASHIWAGI et al. :
Serial No. NEW : **Attn: APPLICATION BRANCH**
Filed March 16, 2004 : **Attorney Docket No. 2004_0363**
A METHOD AND AN APPARATUS FOR :
REPRODUCING BITSTREAM HAVING :
NON-SEQUENTIAL SYSTEM CLOCK :
DATA SEAMLESSLY THEREBETWEEN :
(Rule 1.53(b) Divisional :
of Serial No. 10/335,885, :
Filed January 3, 2003) :
THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
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CLAIM OF PRIORITY UNDER 35 USC 119

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

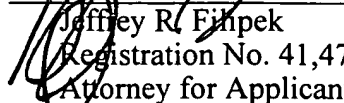
Applicants in the above-entitled application hereby claim the date of priority under the International Convention of Japanese Patent Application No. 7-276710, filed September 29, 1995, and Japanese Patent Application No. 8-41583, filed February 28, 1996, as acknowledged in the Declaration of this application.

Certified copies of said Japanese Patent Applications are of record in parent application Serial No. 08/721,736, filed September 27, 1996, now U.S. Patent No. 5,923,869 which is the parent application of Serial No. 09/195,998, filed November 20, 1998, now U.S. Patent No. 6,393,574 which is the parent application of Serial No. 09/932,983, filed August 21, 2001, which is the parent application of Serial No. 10/335,885, filed January 3, 2003.

Respectfully submitted,

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By


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